

# WEST Search History





DATE: Thursday, September 13, 2007

Hide?	Set Name	Query	Hit Count
		<i>DB=PGPB,USPT; PLUR=YES; OP=OR</i>	
<input type="checkbox"/>	L33	L32 and ("voltage switch\$")[clm]	3
<input type="checkbox"/>	L32	L15 and L23 and L24	187
<input type="checkbox"/>	L31	L15 and (control\$ with "voltage regulator")[clm]	12
<input type="checkbox"/>	L30	L15 and (select\$ with "operating voltage")[clm]	7
<input type="checkbox"/>	L29	L28 and variable[clm]	2
<input type="checkbox"/>	L28	L15 and "voltage regulator"[clm]	25
<input type="checkbox"/>	L27	L26 and L21	0
<input type="checkbox"/>	L26	L15 and ((voltage or supply) adj2 terminal)[clm]	48
<input type="checkbox"/>	L25	L15 and L16 and L20 and L23 and L24	0
<input type="checkbox"/>	L24	(second adj2 (supply or voltage))[clm]	70668
<input type="checkbox"/>	L23	(first adj2 (supply or voltage))[clm]	70454
<input type="checkbox"/>	L22	(enabl\$ with assignment)[clm]	479
<input type="checkbox"/>	L21	(minimum with slack)[clm]	146
<input type="checkbox"/>	L20	slack[clm]	6297
<input type="checkbox"/>	L19	L18 and path[clm]	10
<input type="checkbox"/>	L18	"active blocks"[clm]	82
<input type="checkbox"/>	L17	"active blocks"	1489
<input type="checkbox"/>	L16	"timing analysis"[clm]	513
<input type="checkbox"/>	L15	(pld or "programmable logic device")[clm]	3399
<input type="checkbox"/>	L14	(operat\$ with (pld or "programmable logic device"))[clm]	551
<input type="checkbox"/>	L13	L12 and slack	0
<input type="checkbox"/>	L12	L6 and L11	40
<input type="checkbox"/>	L11	(326/80)[ccls]	512
<input type="checkbox"/>	L10	L9 and slack	33
<input type="checkbox"/>	L9	L6 and (select\$ with voltage)	4186
<input type="checkbox"/>	L8	L7 and slack	9
<input type="checkbox"/>	L7	L6 and (enabl\$ with voltage)	2029
<input type="checkbox"/>	L6	((pld or "programmable logic device" or fpga or "field programmable gate array"))	53853
		<i>DB=USPT,PGPB; PLUR=YES; OP=OR</i>	

- ☐ L5 ('5598348'|'5878053'|'6493859'|'6631502'|'6975978'|'20040172603'|  
'20050091629'|'20050270074'|'20060114025'|'20060117282')![pn] 10  
*DB=PGPB,USPT; PLUR=YES; OP=OR*
- ☐ L4 (logic near block) with (sleep adj transistor) 5
- ☐ L3 ((programmable adj logic adj device) or pld or (field adj programmable adj gate  
adj array) or fpga) 53853
- ☐ L2 ((programmable adj logic adj device) or pld or (field adj programmable adj gate  
adj array) or fpga) same (sleep adj transistor) 1  
(5362989 5519663 5583457 5615162 5682107 5801548 5946257 5958026  
6160418 6169419 6208171 6384626 6466049 6489804 6583645 6710621  
6711719 6747478 6839888 6936917 6950998 6960934 6981160 7003620  
7078932 7080341 7098689 7109748 7112997 7135886 7170315)[pn] 38  
20030218478 20050091547 20050201174 20060053246 20060069851  
20060202713 20070001720

END OF SEARCH HISTORY